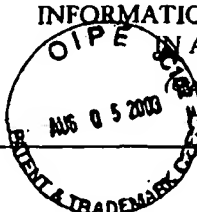
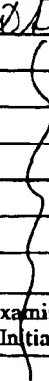


Form PTO-1449 INFORMATION DISCLOSURE CITATION IN AN APPLICATION <small>(See several sheets if necessary)</small>				Docket Number 204552016500		Application Number 09/412,328	
				Applicant <div style="text-align: center; font-weight: bold;">Yuichi SATO</div>			
				Filing Date October 5, 1999		Group Art Unit 2814	
				Mailing Date August 5, 2003			
U.S. PATENT DOCUMENTS							
Examiner Initials	Ref. No.	Date	Document No.	Name	Class	Subclass	Filing Date If Appropriate
FOREIGN PATENT DOCUMENTS							
Examiner Initials	Ref. No.	Date	Document No.	Country	Class	Subclass	Translation YES NO
	1.	3/1979	JP-A-54-037544	Japan			abs.
	2.	8/1986	JP-A-61-185972	Japan			abs.
	3.	1/1991	JP3022476	Japan			abs.
	4.	2/1992	JP-A-04-053090	Japan			abs.
	5.	6/1995	JP-A-07-161844	Japan			abs.
	6.	5/1996	JP-A-07-176633	Japan			abs.
	7.	8/1998	JP-A-10-222985	Japan			abs.
OTHER DOCUMENTS <small>(including author, title, Date, Pertinent Pages, Etc.)</small>							
Examiner Initials	Ref. No.	Title					
	8.	F. Assaderaghi et al., "Dynamic threshold-voltage MOSFET (DTMOS) for ultra-low voltage VLSI", IEEE Transactions on Electron Devices, vol. 44, no. 3, pp. 414-422, March 1997					
<div style="display: flex; justify-content: space-between;"> <div style="width: 45%;"> EXAMINER: <i>Douglas A. Wille</i> </div> <div style="width: 45%;"> 1 DATE CONSIDERED: <i>15 Nov 03</i> </div> </div>							
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